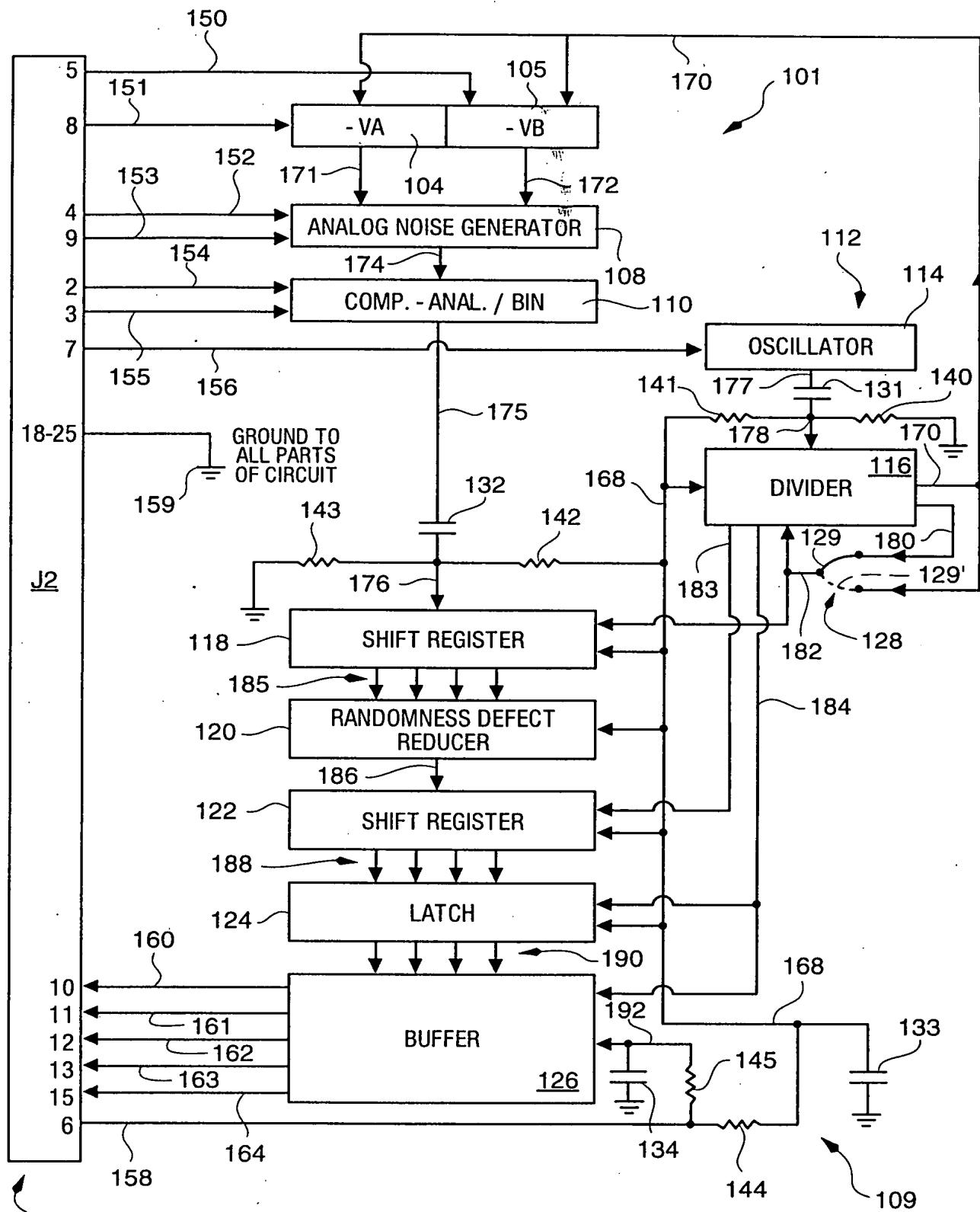


6763364



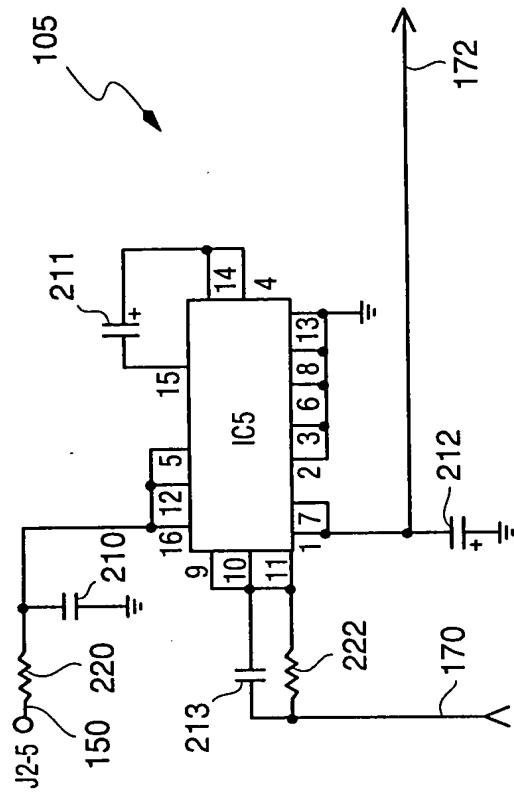


FIG. 2

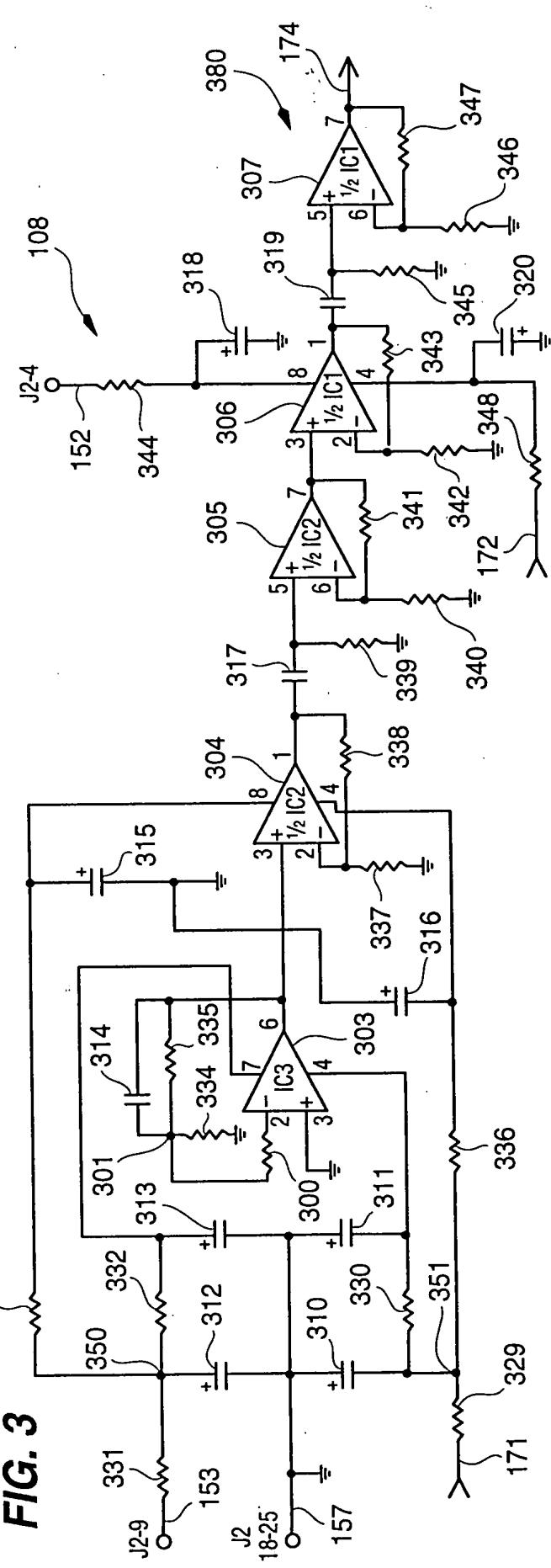


FIG. 3

FIG. 4

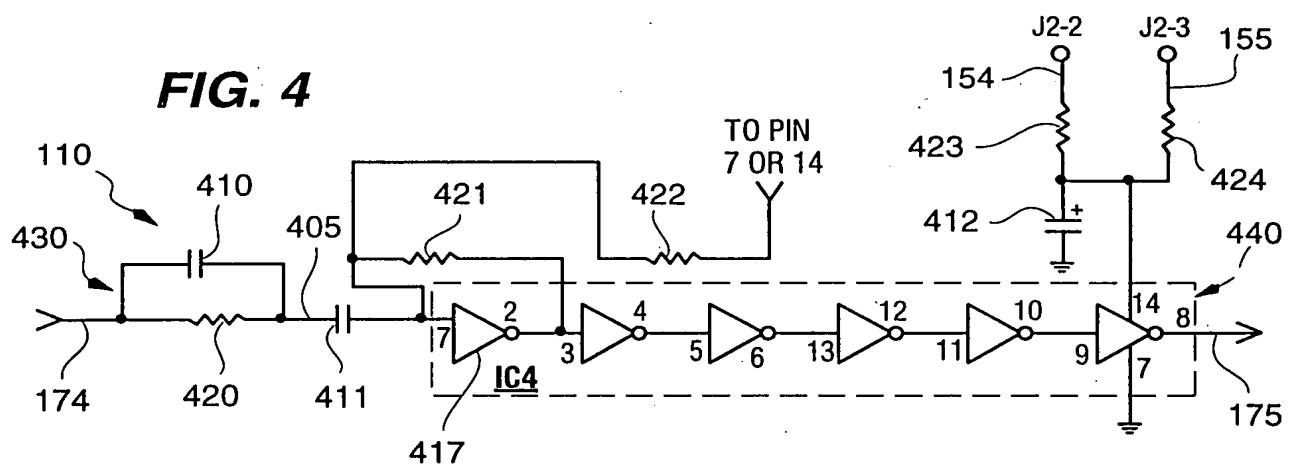


FIG. 5

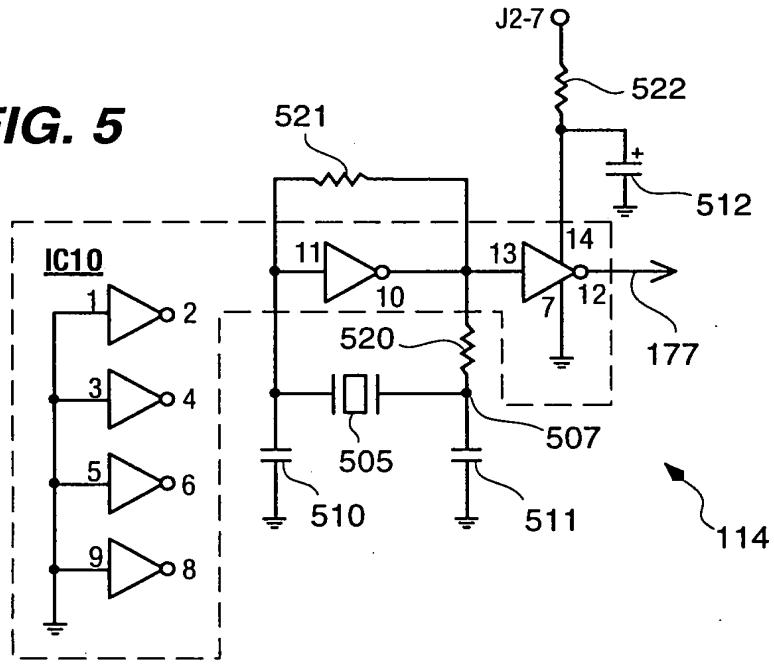
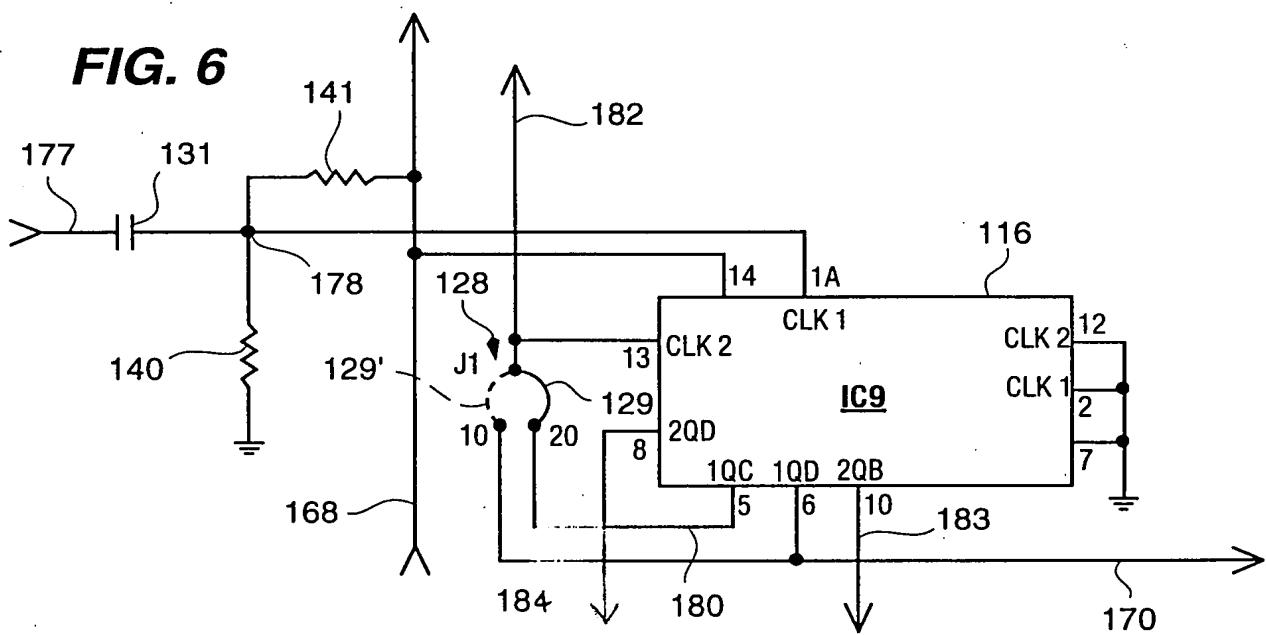
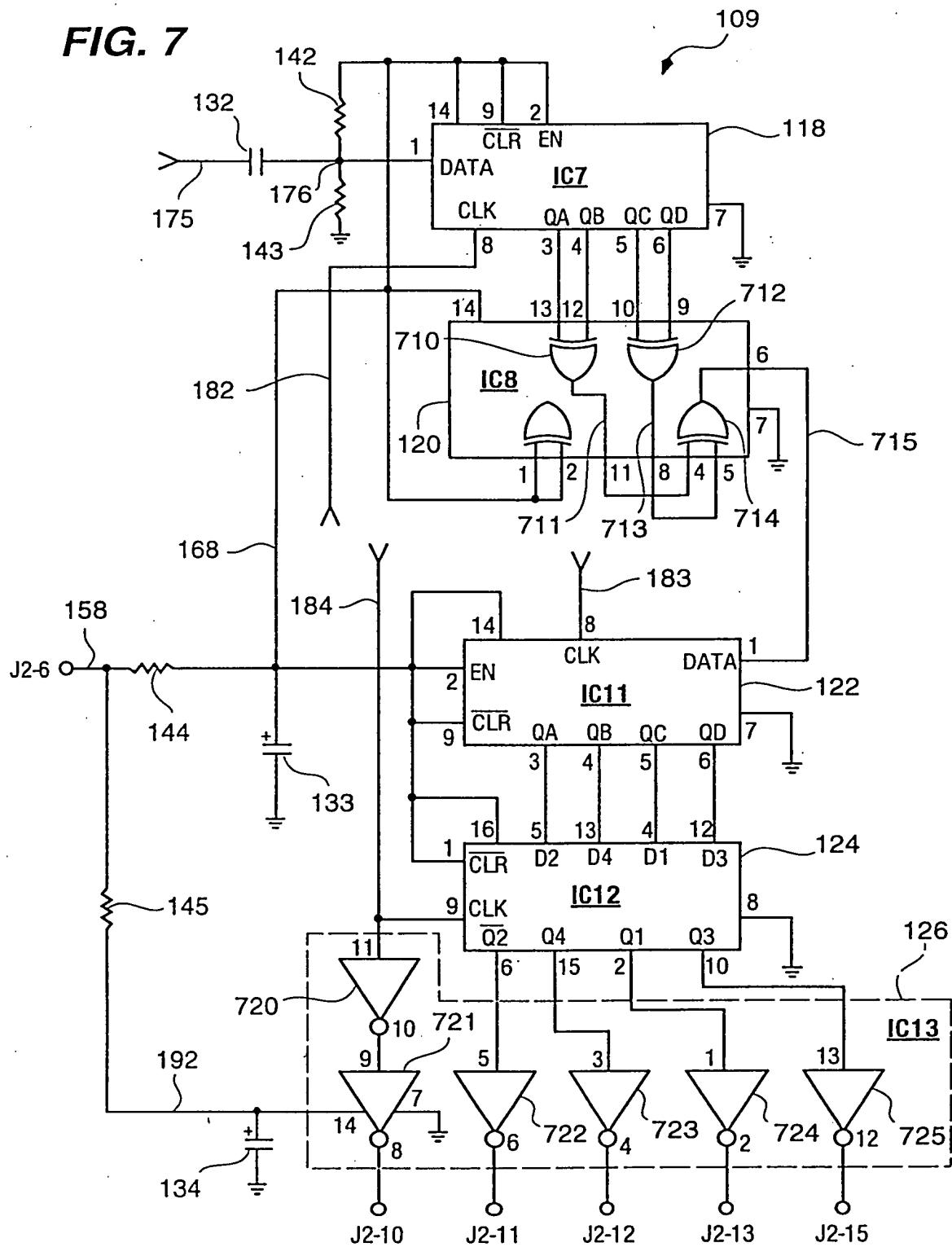


FIG. 6



00000000000000000000000000000000

FIG. 7



00000000000000000000000000000000

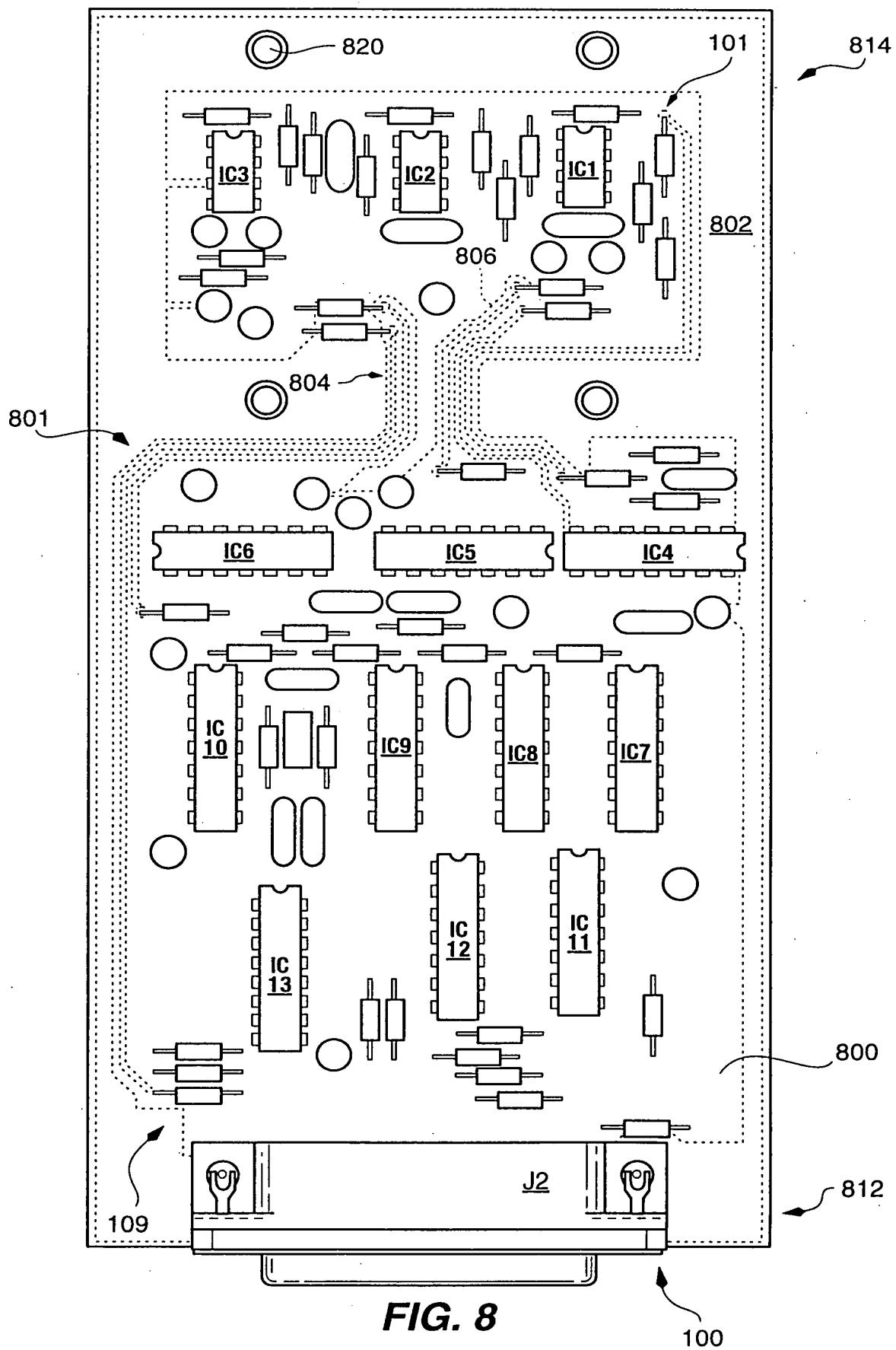


FIG. 8

000000000000000000000000

FIG. 9

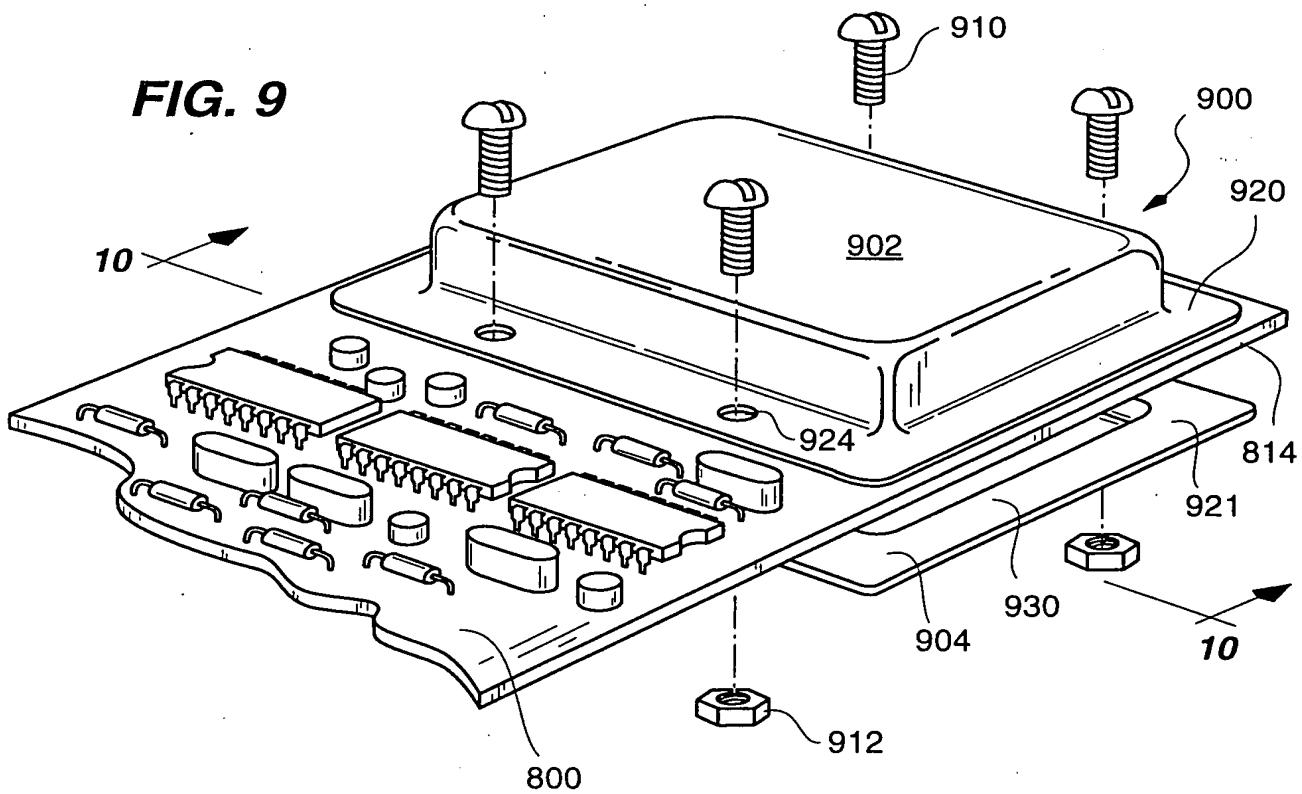
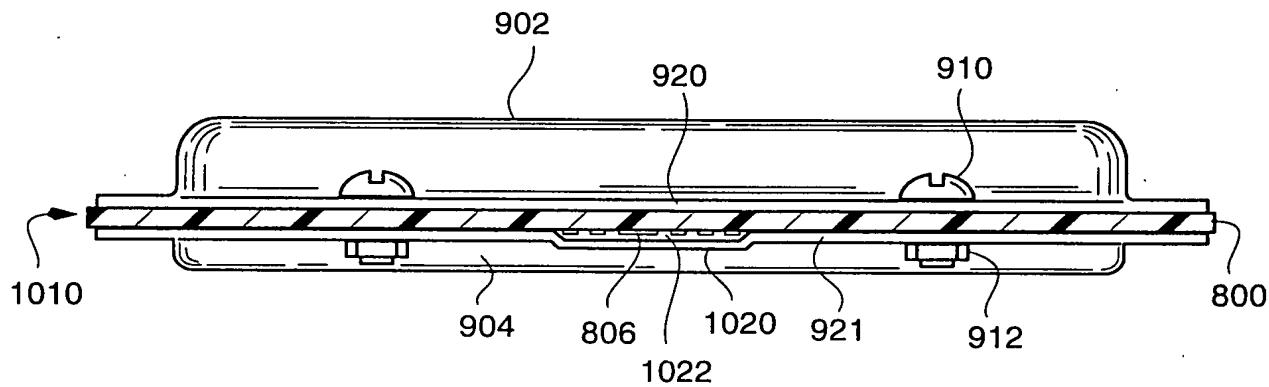


FIG. 10



000000000000000000000000

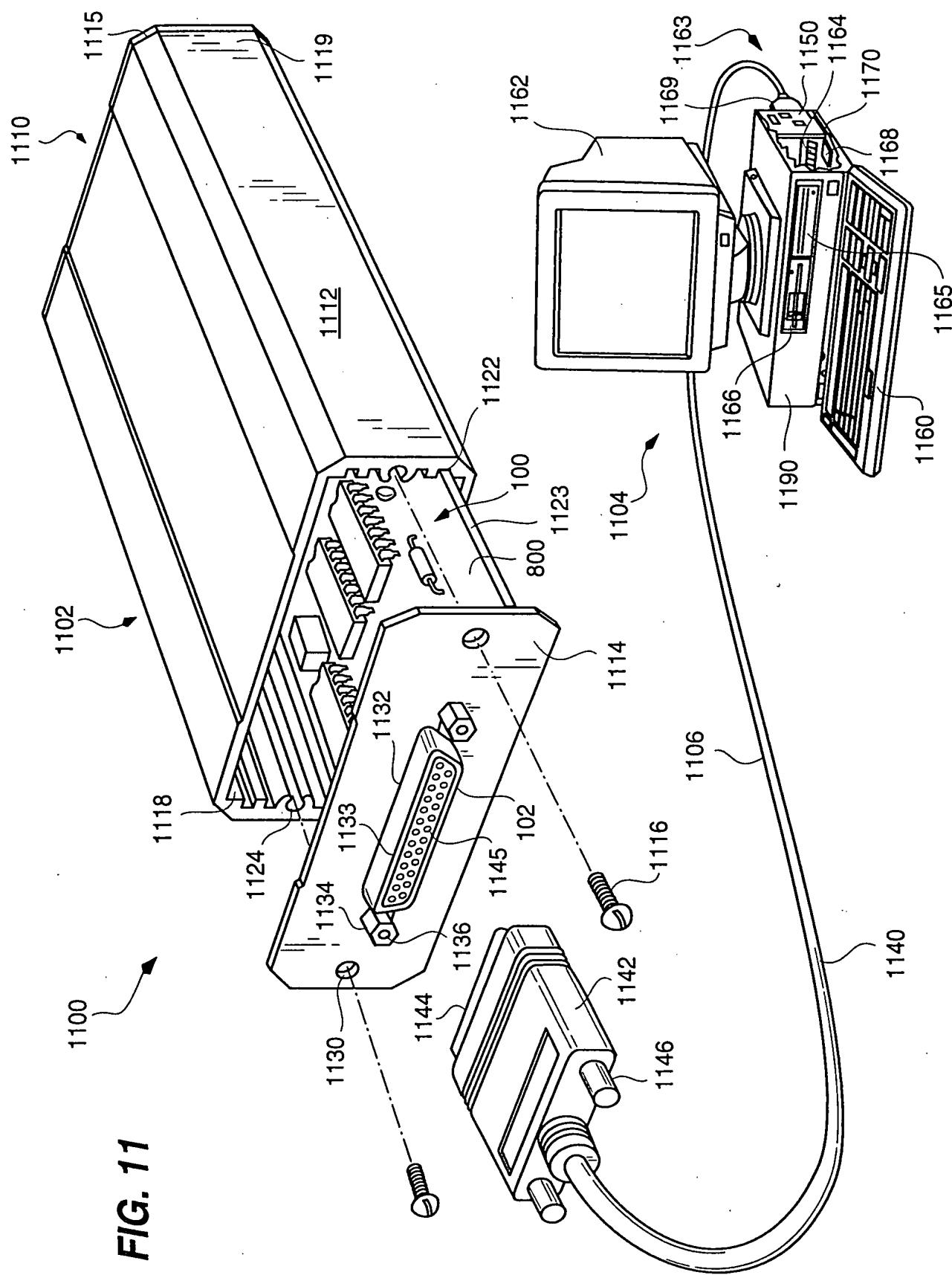


FIG. 11

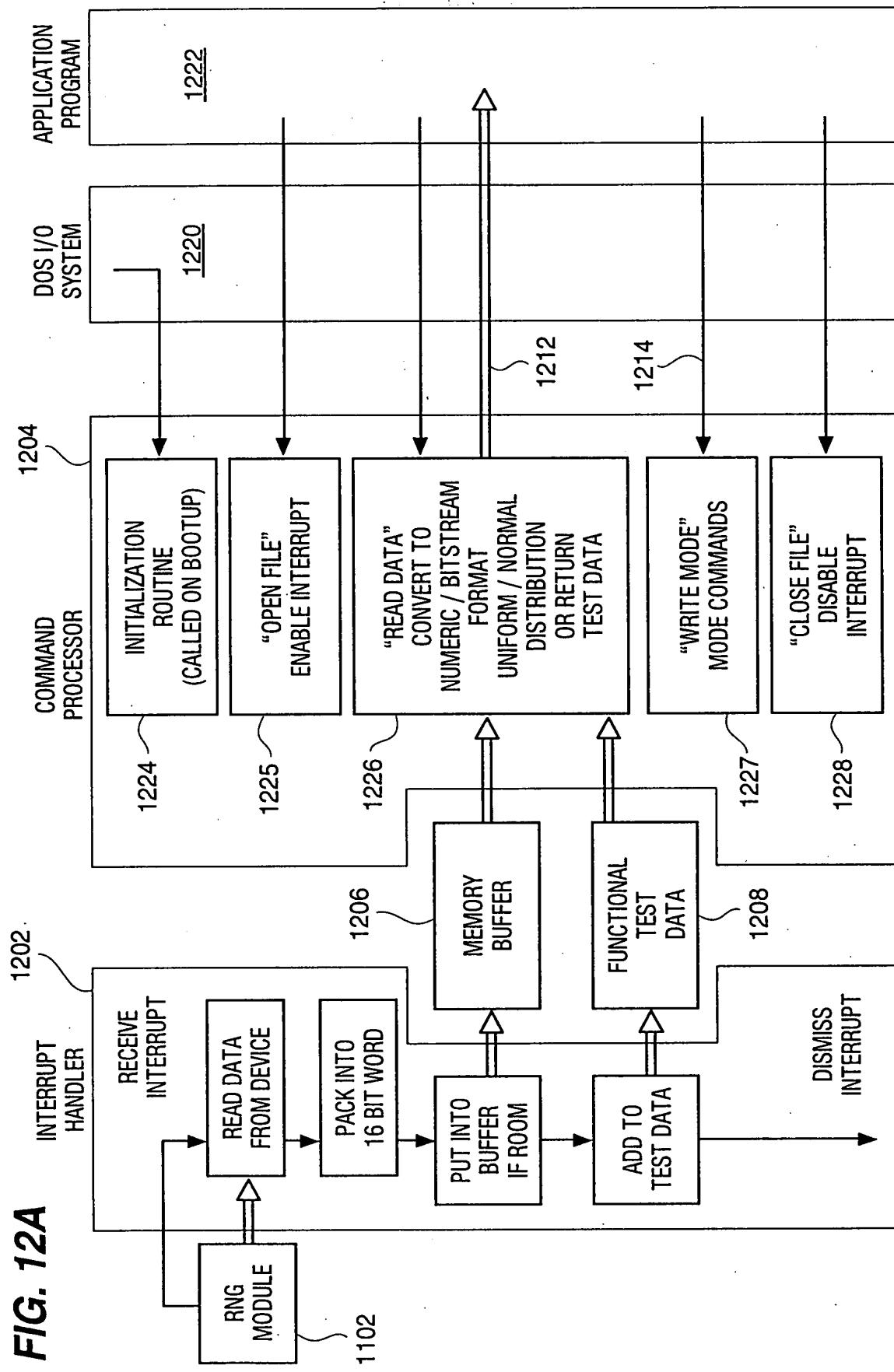
FIG. 12A

FIG. 12B

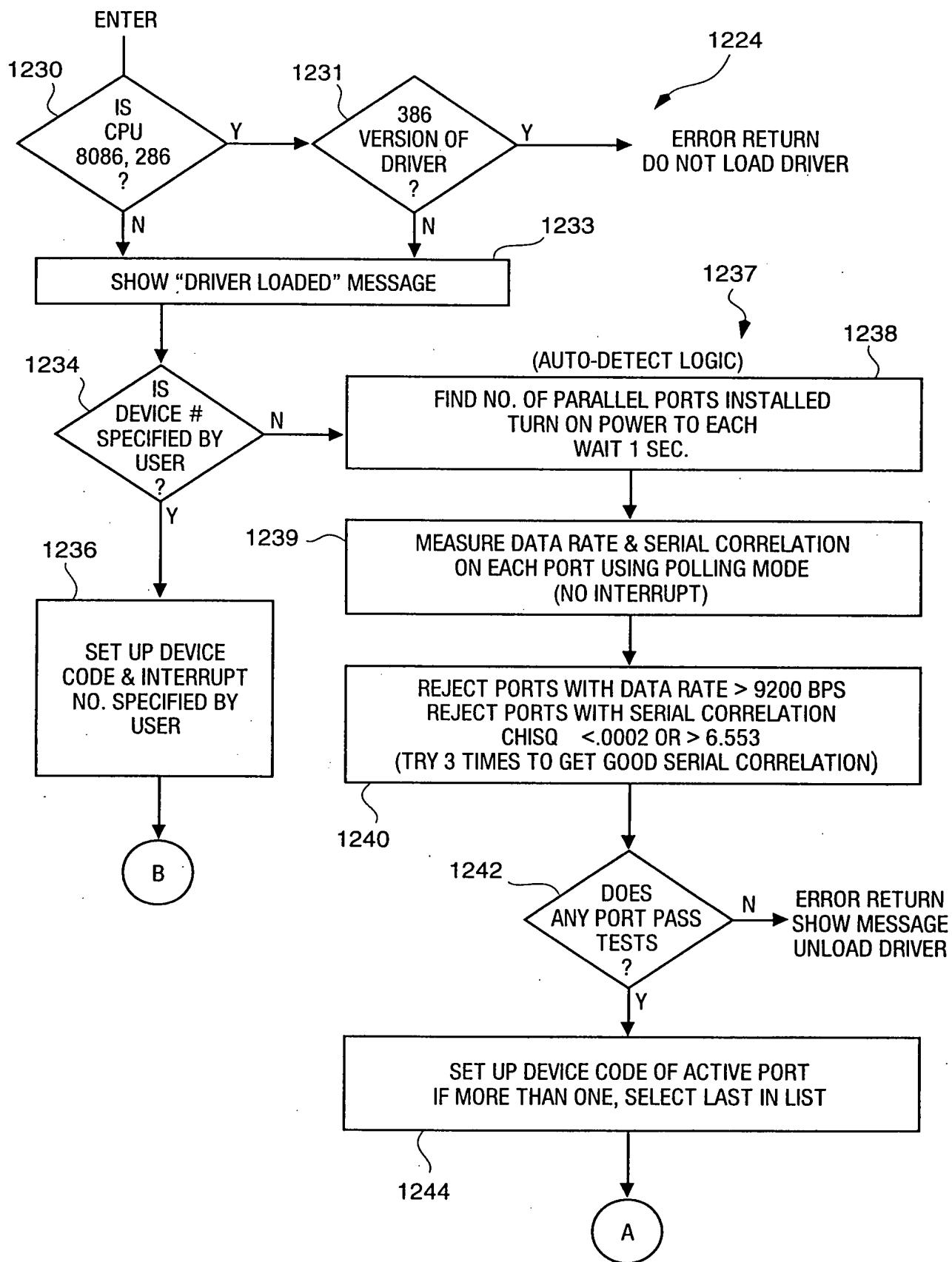


FIG. 12C

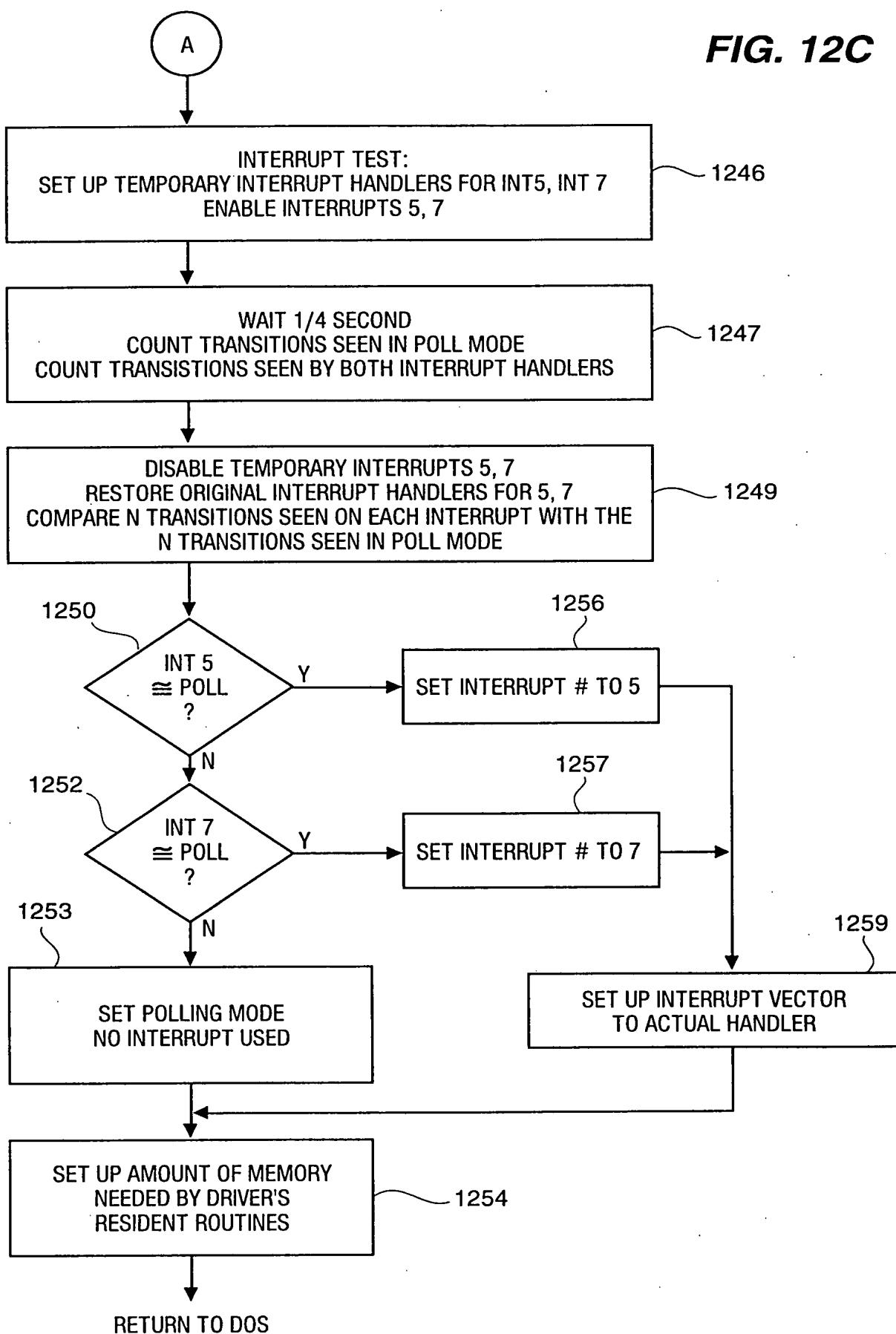
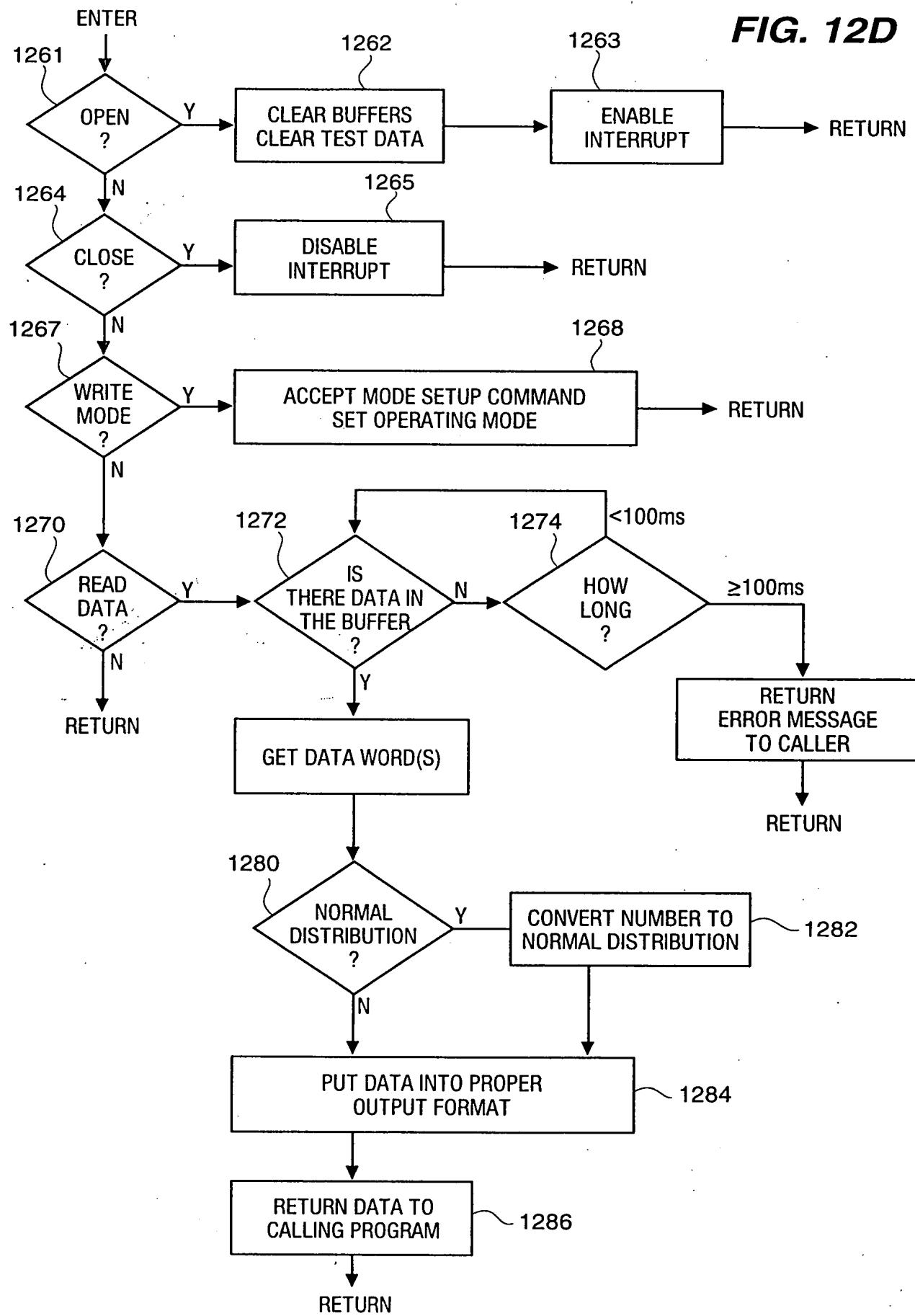


FIG. 12D



COMPENSATED AND UNCOMPENSATED OUTPUTS

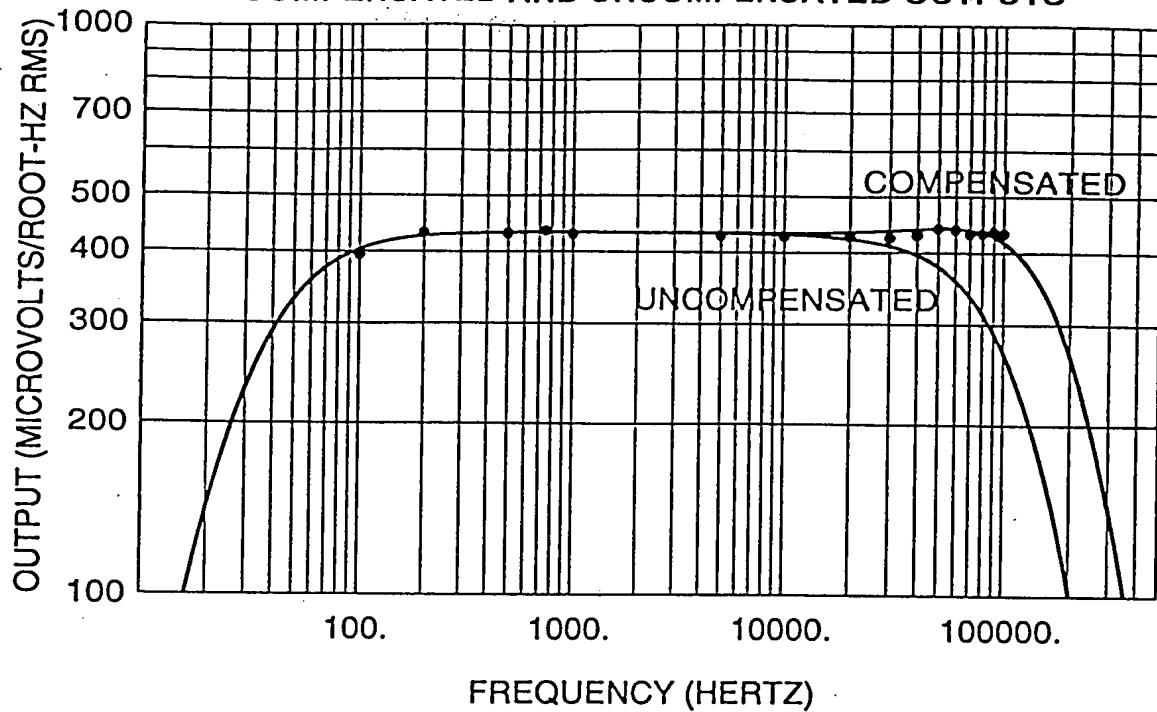


FIG. 13

SERIAL DEPENDENCE OF BINARY OUTPUT

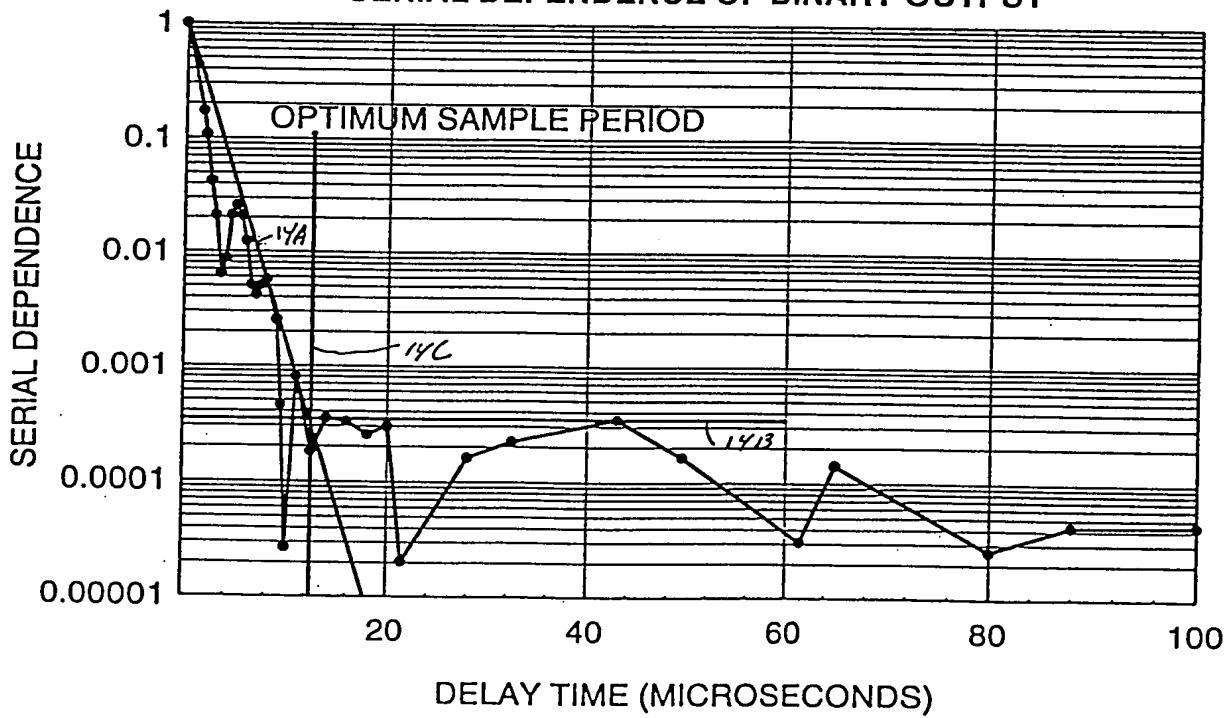


FIG. 14

00000T-E2566960

FIG. 15

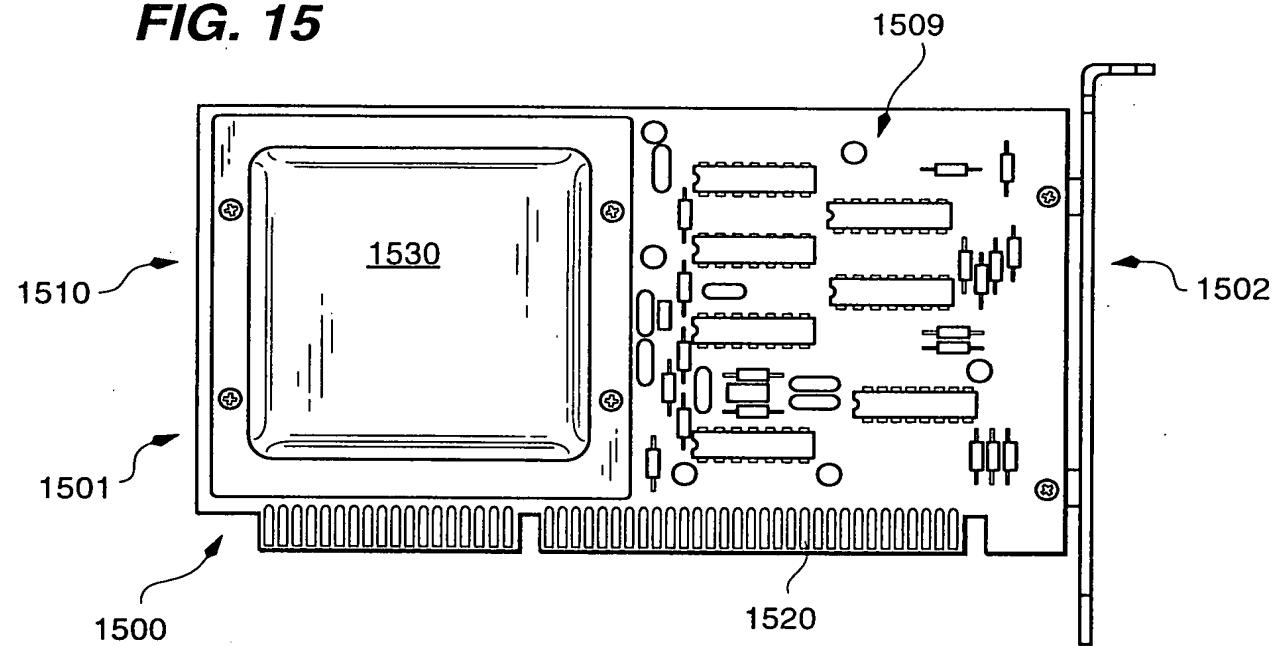
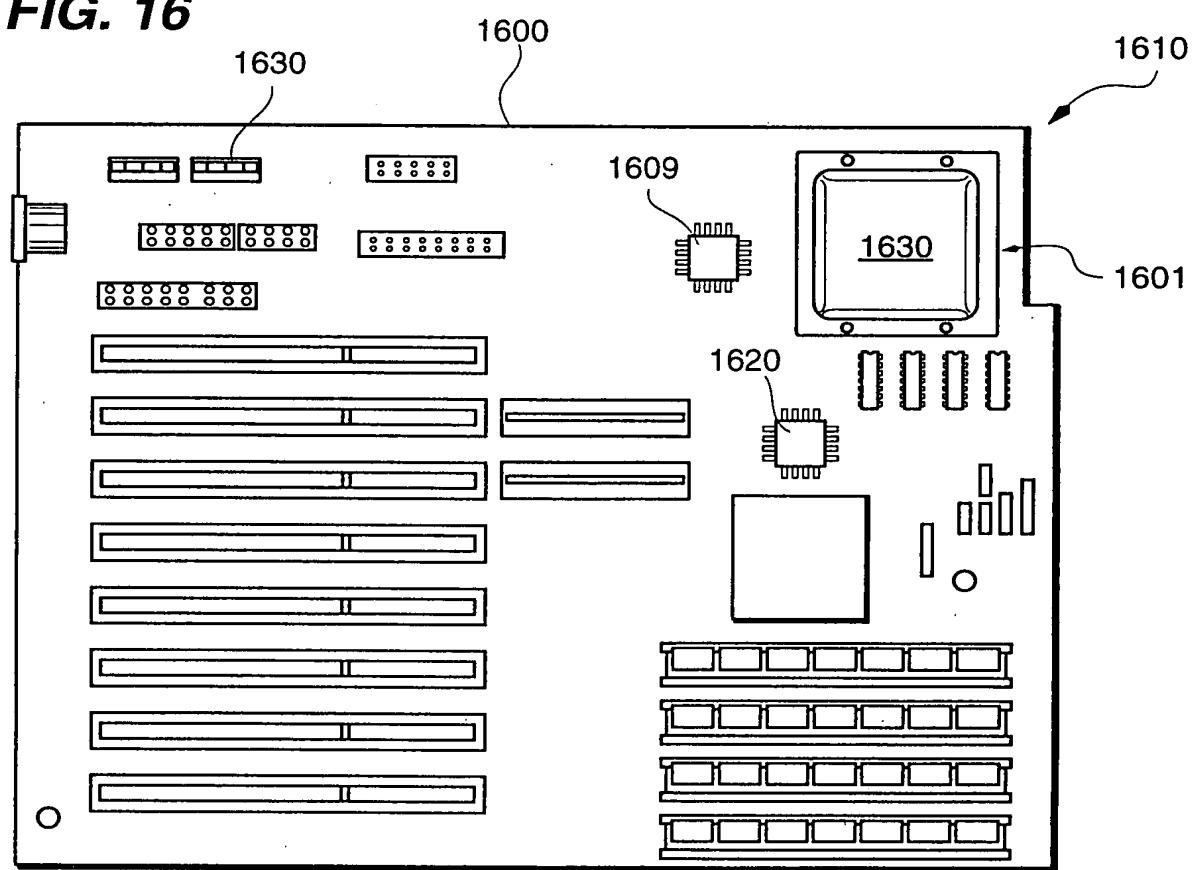


FIG. 16



000001-22566960

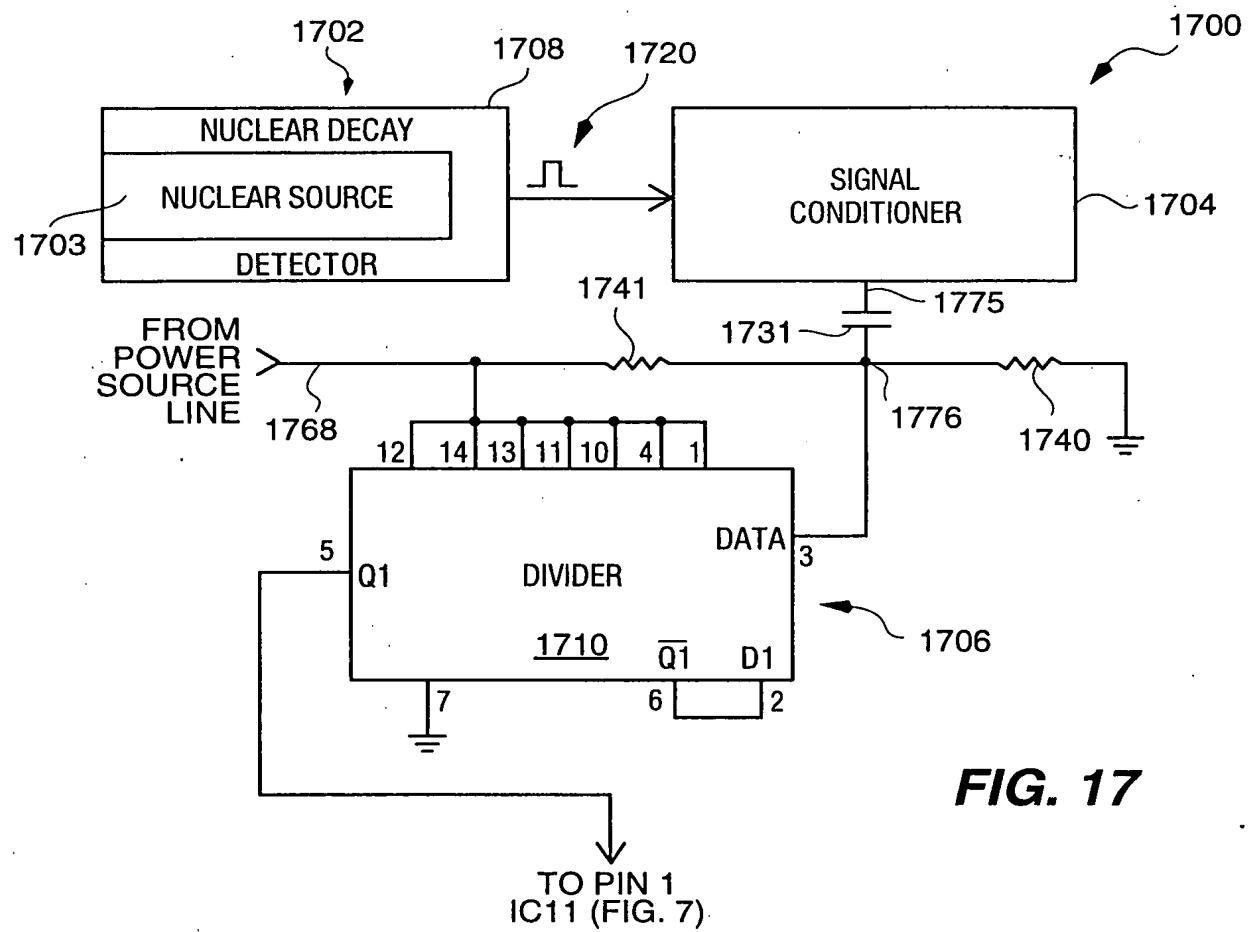


FIG. 17

